

Fig. 1 PRIORITY ART

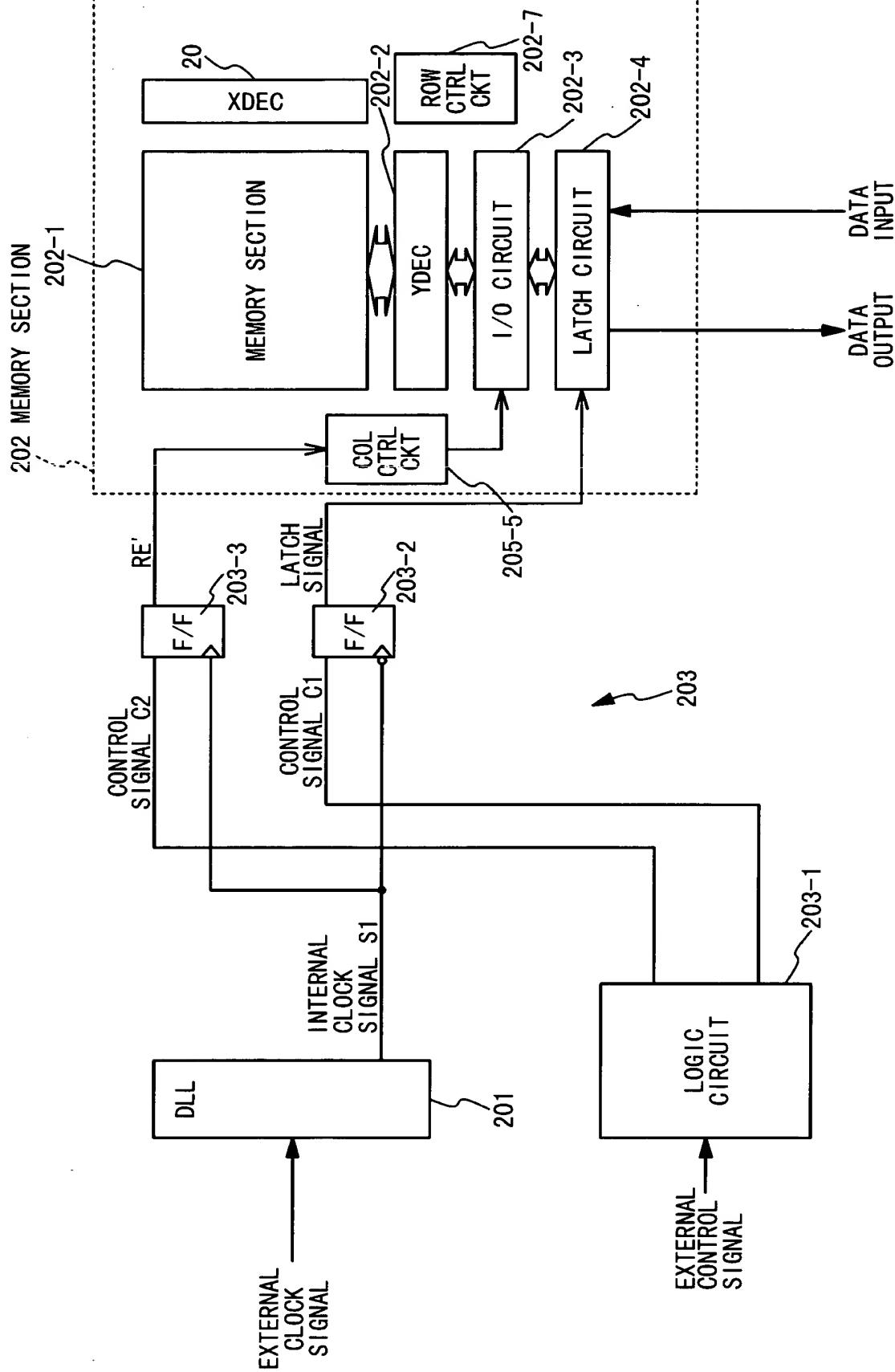


Fig. 2A
PRIOR ART

INTERNAL CLOCK SIGNAL S₁

Fig. 2B
PRIOR ART

EXTERNAL SIGNAL C₂
CONTROL SIGNAL READ COMMAND

Fig. 2C
PRIOR ART

EXTERNAL SIGNAL C₂
CONTROL SIGNAL

Fig. 2D
PRIOR ART

EXTERNAL SIGNAL C₂
CONTROL SIGNAL

Fig. 2E
PRIOR ART

EXTERNAL SIGNAL C₂
CONTROL SIGNAL

Fig. 2F
PRIOR ART

EXTERNAL SIGNAL C₁
CONTROL SIGNAL

Fig. 2G
PRIOR ART

EXTERNAL SIGNAL C₁
CONTROL SIGNAL

Fig. 2H
PRIOR ART

EXTERNAL SIGNAL C₁
CONTROL SIGNAL

Fig. 2I
PRIOR ART

EXTERNAL SIGNAL C₁
CONTROL SIGNAL

Fig. 2J
PRIOR ART

EXTERNAL SIGNAL C₁
CONTROL SIGNAL

Fig. 2K
PRIOR ART

EXTERNAL SIGNAL C₁
CONTROL SIGNAL

Fig. 2L
PRIOR ART

EXTERNAL SIGNAL C₁
CONTROL SIGNAL

Fig. 2M
PRIOR ART

EXTERNAL SIGNAL C₁
CONTROL SIGNAL

Fig. 2N
PRIOR ART

EXTERNAL SIGNAL C₁
CONTROL SIGNAL

LATCH SIGNAL

Fig. 3 PRIOR ART

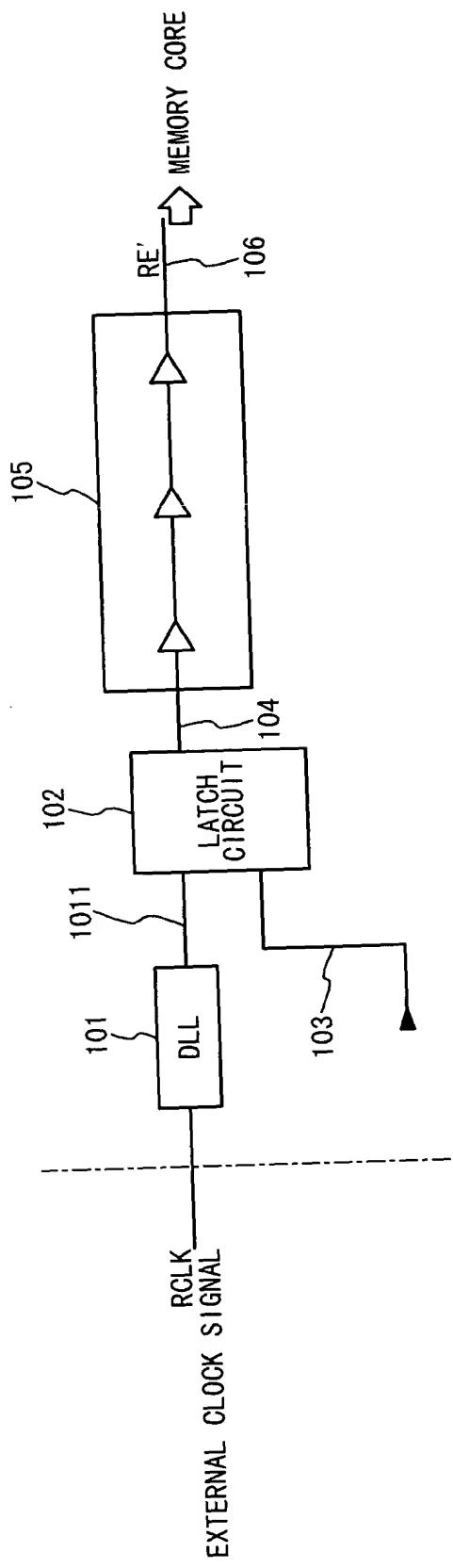


Fig. 4 A

INTERNAL
CLOCK SIGNAL S1

Fig. 4 B

INTERNAL
CLOCK SIGNAL S2

Fig. 4 C

SIGNAL
USING SIGNAL S1

PRIOR ART

SIGNAL
USING SIGNAL S2

Fig. 4 D

SIGNAL 104 (106)

PRIOR ART

104 (106)

108

107

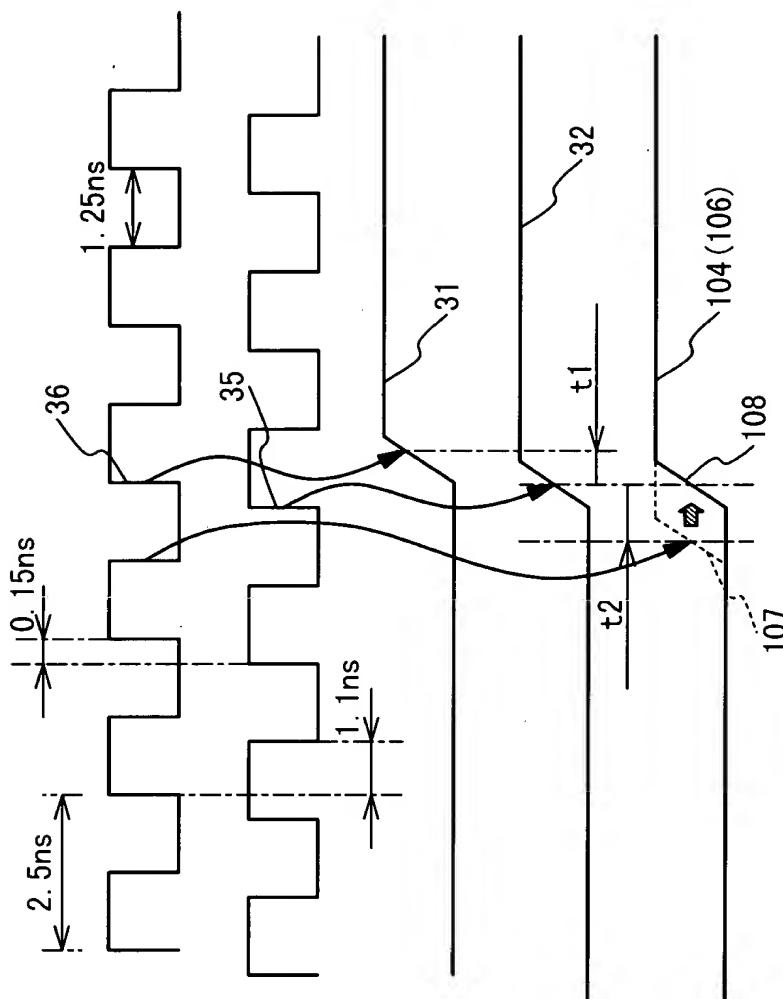
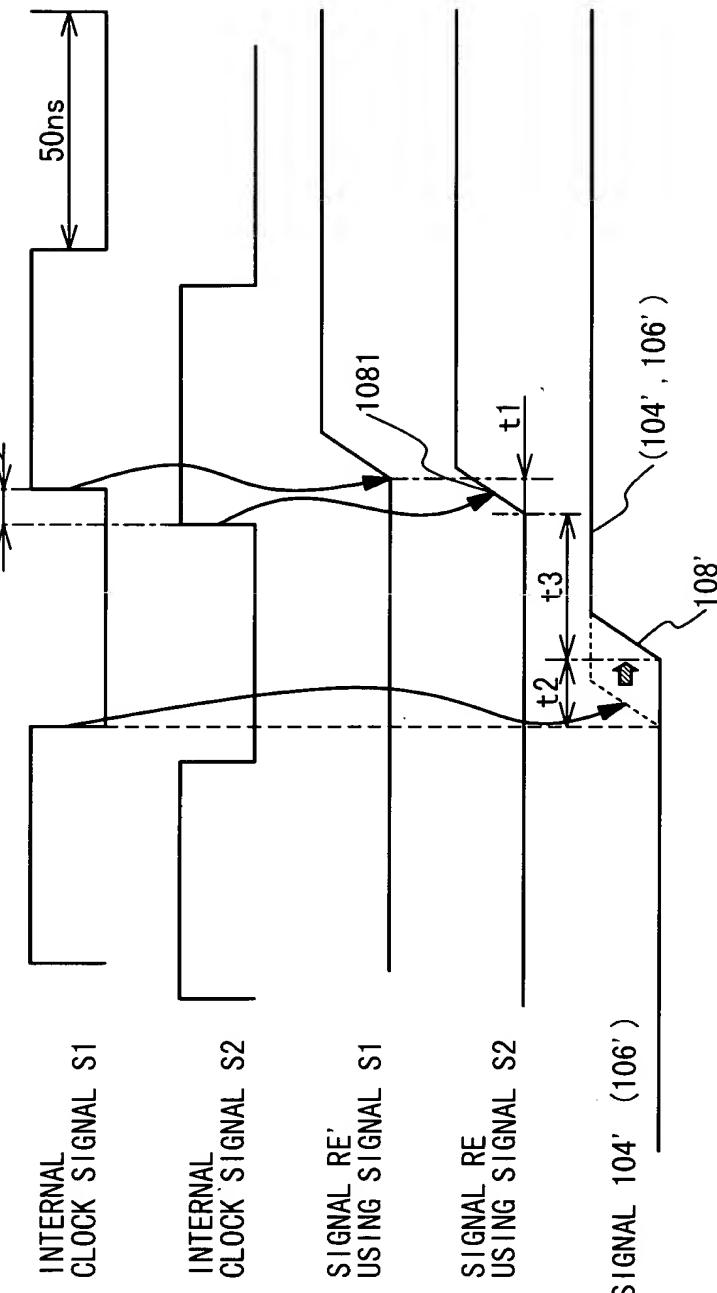


Fig. 5 A
Fig. 5 B
Fig. 5 C
PRIOR ART
PRIOR ART

CONSTANT INDEPENDENT OF FREQUENCY



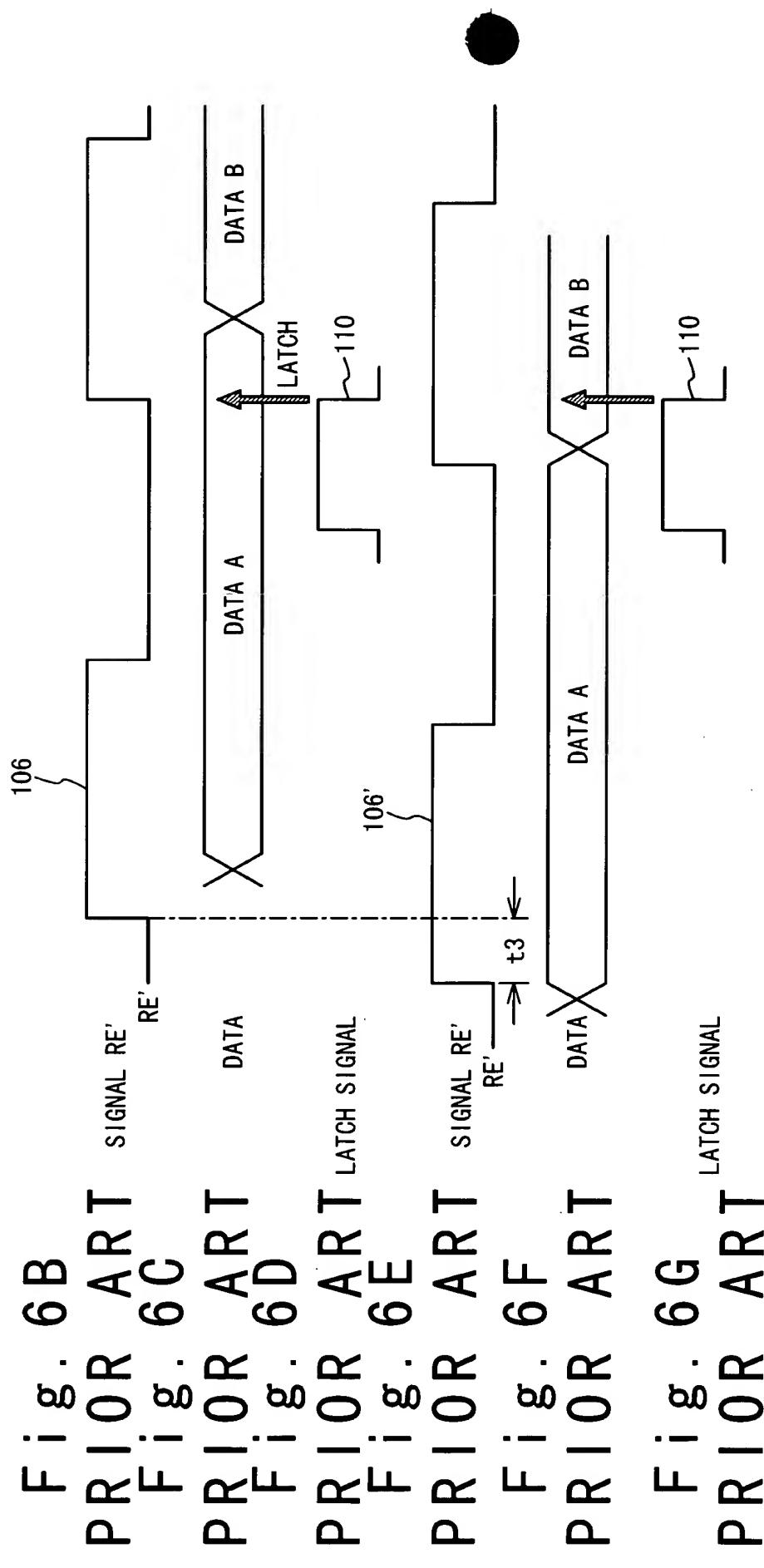
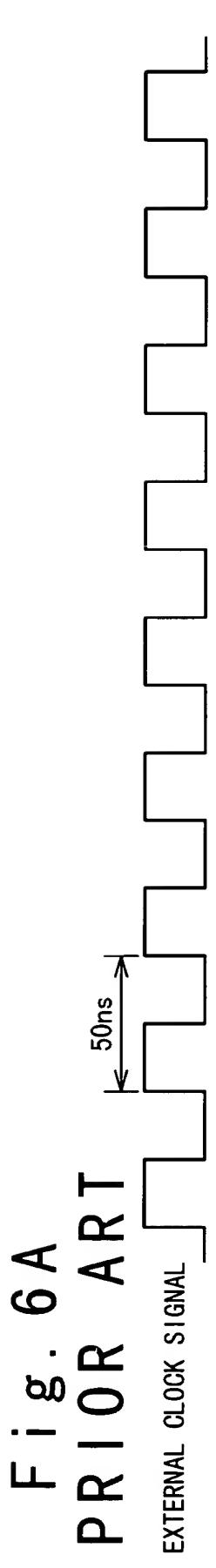


Fig. 7A

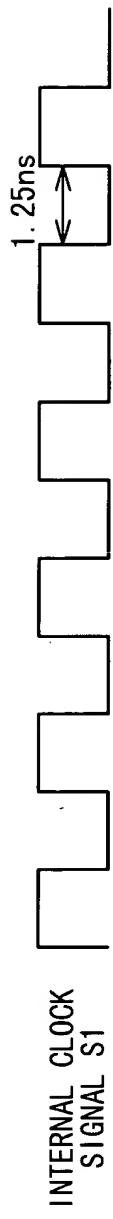


Fig. 7B



Fig. 7C

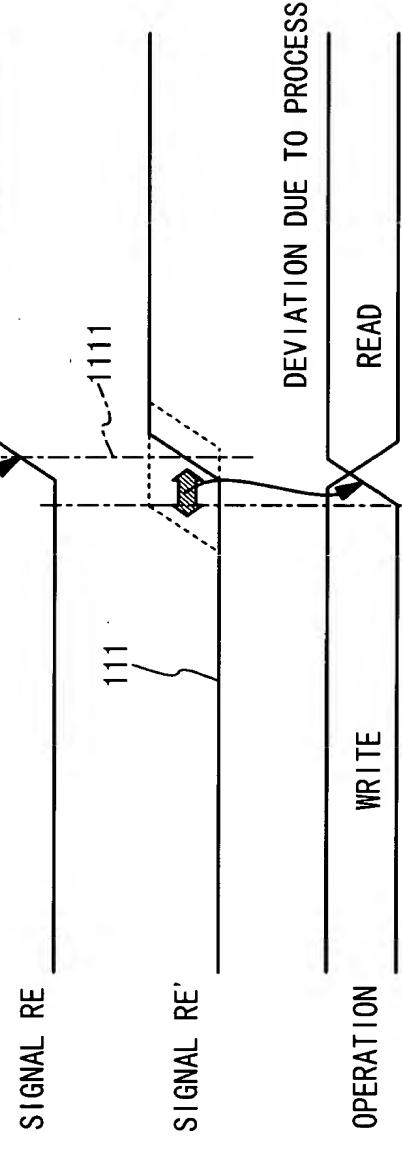


Fig. 7D

PR | OR ART

Fig. 7E

PR | OR ART

Fig. 7F

Fig. 8A

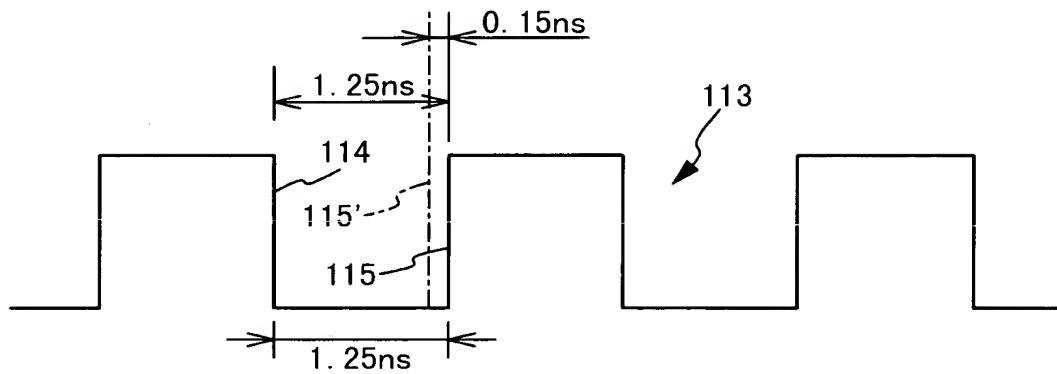


Fig. 8B PRIOR ART

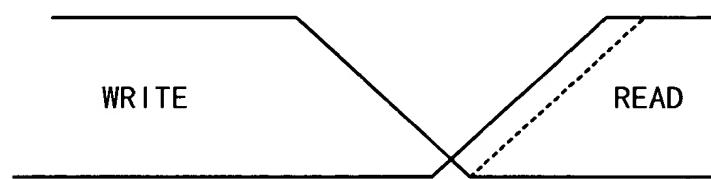


Fig. 8C PRIOR ART

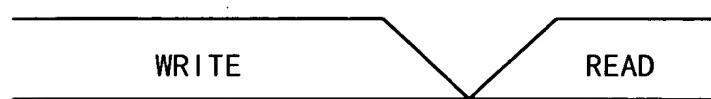


Fig. 9 PRIOR ART

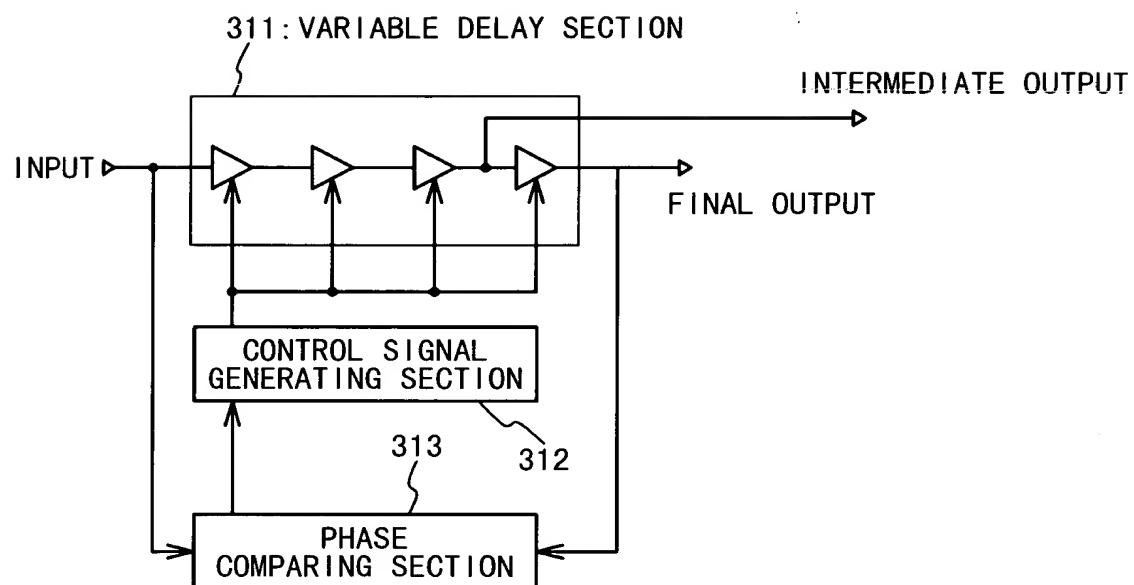


Fig. 10

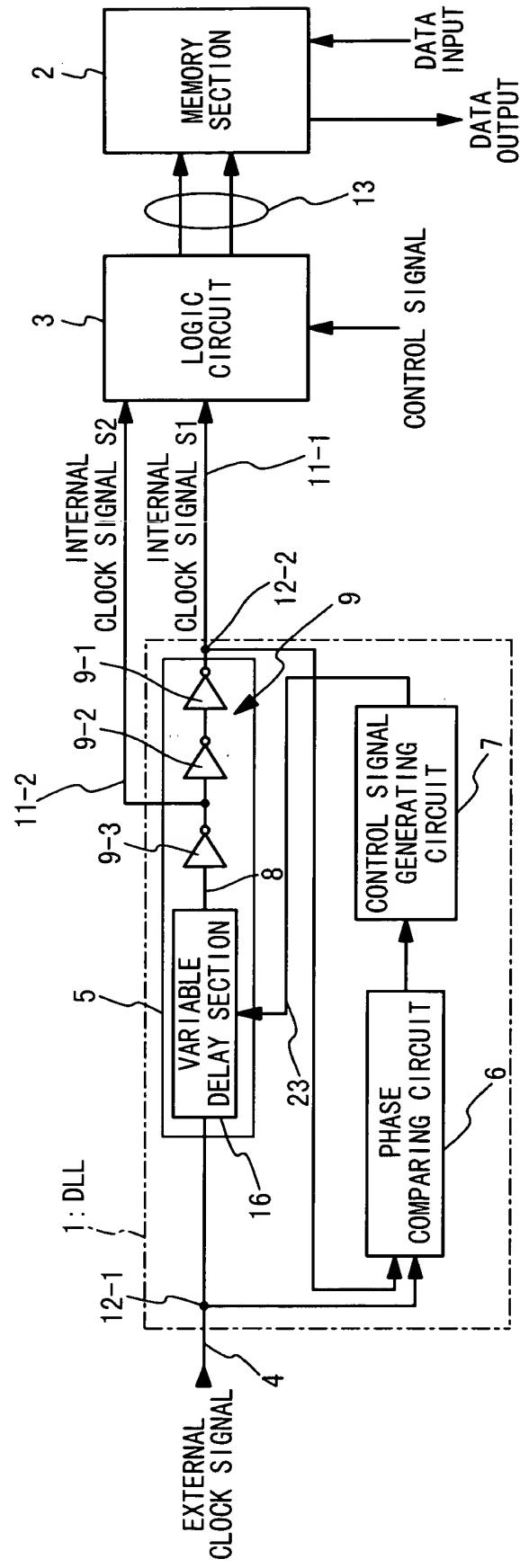


Fig. 11

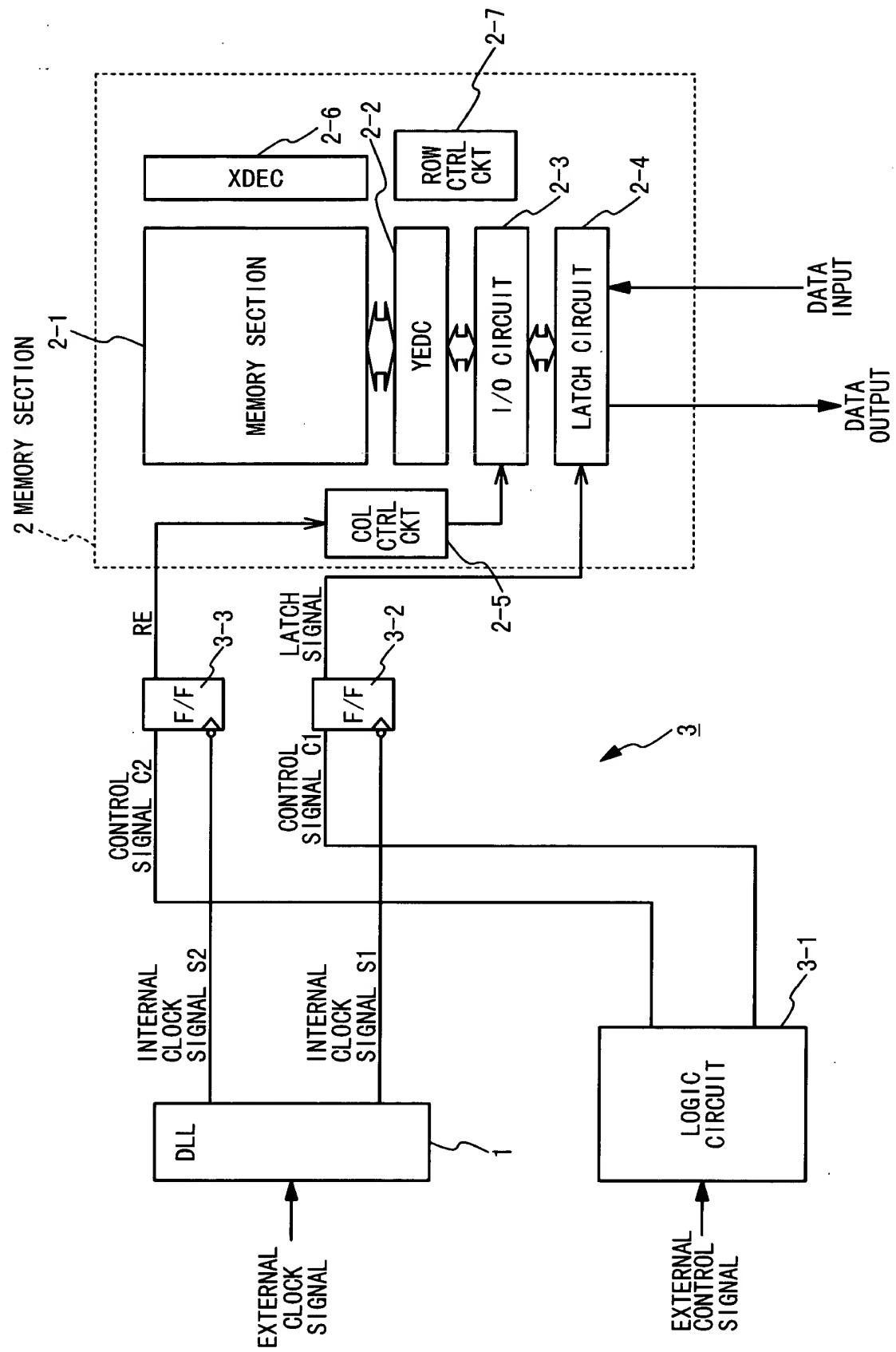


Fig. 12 A

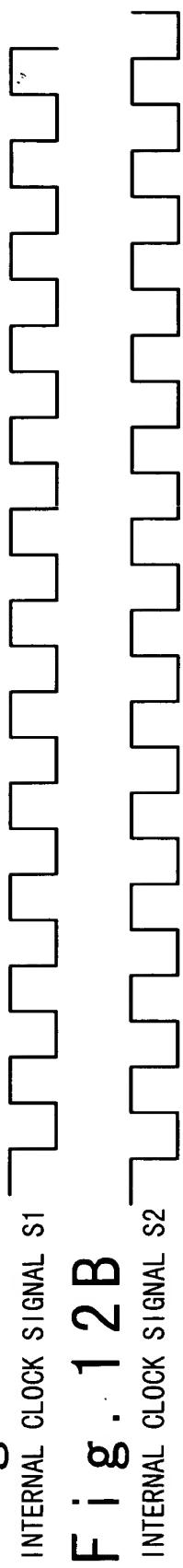


Fig. 12 B

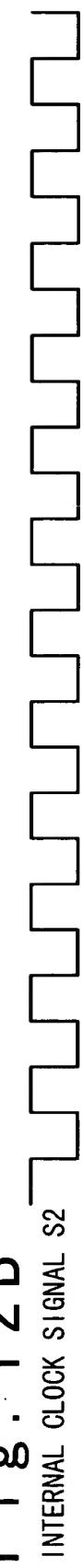


Fig. 12 C

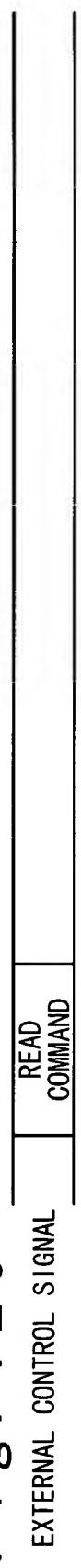


Fig. 12 D



Fig. 12 E



Fig. 12 F



Fig. 12 G



Fig. 12 H

